

Data sheet (lab 3)

Course: CSE209 Electrical Circuits

Experimental name: Bias Point Detail Analysis of DC Circuit with Independent Sources Using PSpice Schematics

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**Simulation Diagram:**

**Diagram

Description automatically generated with medium confidence**

Figure 1-Circuit Diagram

**Schematics Netlist:**

V\_Vs $N\_0001 0 20V

R\_Ra $N\_0001 $N\_0002 5K

R\_Rc 0 $N\_0003 1k

I\_Is $N\_0003 $N\_0002 DC 2mA

R\_Rb 0 $N\_0002 4K

**Result of simulation:**

A picture containing application

Description automatically generated

Figure 2-Result of simulation

Schematics Netlist:

I\_I2 $N\_0001 0 DC -10A

R\_R1 $N\_0003 $N\_0002 20ohm

R\_R2 $N\_0001 $N\_0002 30ohm

R\_R4 $N\_0001 0 10ohm

R\_R3 0 $N\_0002 10ohm

V\_V1 $N\_0003 0 10V

I\_I3 $N\_0002 $N\_0001 DC 3A